

Bhoj Reddy Engineering College for Women
Department of Electronics & Communication Engineering
Time Table for the Academic Year 2015-16
II B.Tech I semester ECE Section-B

Room No : 304 North Block (NB)

w.e.f: 27-07-2015

Day	930-1030	1030-1130	1130 - 1230	1230 – 1330	1330 - 1430	1430-1530	1530-1630
Monday	EC	PTSP	SS	LUNCH	M -III	STLD(G1-304) M-III(G2-309) SS(G3-311)	STLD
Tuesday	EDC	STLD	M-III(G1-304) SS(G2-309) STLD(G3-311)		SS	EC	PTSP
Wednesday	SS	EC	PTSP(G1-304) EC(G2-309) EDC(G3-311)		EDC/BS LAB		
Thursday	M -III	STLD	EDC(G1-304) PTSP(G2-309) EC(G3-311)		PTSP	EDC	EC
Friday	PTSP	M -III	EDC		EC(G1-304) EDC(G2-309) PTSP(G3-311)	STLD	SS
Saturday	BS/EDC LAB				EDC	SS(G1-304) STLD(G2-309) M-III(G3-311)	M -III

S.No	Subject		Faculty	Dept
1	M III	Mathematics -III	M.Sathish	Maths
2	PTSP	Probability Theory & Stochastic Processes	G.Srilakshmi	ECE
3	STLD	Switching Theory & Logic design	S.Prashanthi	ECE
4	EC	Electrical Circuits	T.Vinay Kumar	EEE
5	EDC	Electronic devices & circuits	M.Swapna	ECE
6	SS	Signals & Systems	K.Chandrashekar Reddy	ECE
7	EDC LAB	Electronic devices & circuits Lab	M.Swapna, Neha Jain/A.Navila	ECE
8	BS LAB	Basic Simulation Lab	K.Chandrashekar Reddy, Radhika.R/Saba Sulthana	ECE

Tutorial Groups :

G1: 461-485

G2: 486-4A9

G3: 4B0-Last Number

Incharge
B.Prabhavathi/S.Deepika

HOD
Dr.K.Yedukondalu

Principal
Dr.G.Ravinder Reddy